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| Expt No | Expt Name |
|  | Implementation of Booth’s Multiplier |
|  | Implementation of Bitwise Division algorithm |
|  | Adder |
|  | ALU – 4 bits |
|  | Controller Design – Booth |
|  | Fully Associative Cache |
|  | Direct Mapped Cache |
|  | 2/ 4Way set Associative Cache |
|  | Virtual Memory simulation |
|  | LRU implementation |
|  | Study on Dynamic Branch Prediction |

COA Practical List